

5. (Three Times Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including processor instructions and a program;

performing an error correction routine by processing at least a portion of said information transmission;

programming said receiver station to perform a failure handling routine in accordance with said processor instructions of said information transmission as corrected in said step of performing;

discerning a failure evidencing at least one of incomplete programming and an incorrect program element in said memory by processing information received in said information transmission as corrected in said step of performing; and

executing said failure handling routine in consequence of said step of discerning a failure; wherein said method controls said receiver station.

6. (Three Times Amended) The method of claim 5, wherein said failure handling routine comprises the step of:

clearing at least a portion of said memory.

7. (Three Times Amended) The method of claim 5, further comprising the step of:  
placing data at said memory to produce programming in consequence of said step of executing a failure handling routine.

8. (Three Times Amended) The method of claim 5, further comprising the step of:

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interrupting a processor in accordance with said failure handling routine.

9. (Amended) The method of claim 5, wherein said failure handling routine causes said processor to select a code designating an instruction to be executed, and jump to a memory location based on said selected code.

10. (Amended) The method of claim 9, wherein said code is selected by computing a target number.

11. (Twice Amended) The method of claim 5, further comprising the steps of:  
storing history-of-efficiency information; and  
restoring functionality of said at least one processor based on said stored history of efficiency information.

12. (Unchanged) The method of claim 5, wherein said step of discerning a failure comprises comparing information stored at a first memory location to information stored at a second memory location.

13. (Amended) The method of claim 12, wherein at least one of said first memory location and said second memory location comprises a dedicated register at said at least one processor.

E5 14. (Amended) The method of claim 5, wherein said error correction routine includes forward error correction.

Please cancel claim 15.

16. (Three Times Amended) The method of claim 11, further comprising the step of:  
altering said history-of-efficiency information to reflect said step of discerning a failure.

17. (Three Times Amended) The method of claim 7, wherein said produced programming comprises video.

E6 18. (Amended) The method of claim 5, wherein said program comprises mass medium programming.

19. (Amended) The method of claim 5, wherein said program includes video.

20. (Amended) The method of claim 5, wherein said program comprises a computer program.

Please cancel claim 21.

E7 22. (Amended) The method of claim 5, wherein said step of programming said receiver station comprises:

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receiving said failure handling routine from a remote station;  
directing said received failure handling routine to a programmable device; and  
storing said [at least some of said primary error correction routine and said secondary error correction] said received failure handling routine at said programmable device.

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Please cancel claims 23 and 24 ✓

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25. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including mass medium programming including audio programming;

performing an error correction routine by processing at least a portion of said information transmission;

discerning a failure evidencing at least one of incomplete programming and an incorrect mass medium programming element in said memory by processing information received in said information transmission as corrected in said step of performing; and

executing a failure handling routine in consequence of said step of discerning a failure;

wherein said method controls said receiver station.

26. (Amended) The method of controlling a receiver station of claim 25, wherein said step of executing a failure handling routine further includes the step of:

at least one of completing, correcting and discarding at least a portion of said mass medium programming including said audio programming.

27. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including computer programming which is capable of programming said receiver station;

performing an error correction routine by processing at least a portion of said computer programming;

discerning a failure evidencing at least one of incomplete programming and an incorrect program element in said memory by processing said computer programming received in said information transmission as corrected in said step of performing; and

executing a failure handling routine in accordance with said received computer programming; wherein said method controls said receiver station.

28. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including a program;

performing an error correction routine by processing at least a portion of said information transmission;

discerning a failure evidencing at least one of incomplete programming and an incorrect program element in said memory by processing information received in said information transmission as corrected in said step of performing;

selecting at least one of a plurality of failure handling routines to execute in consequence of said step of discerning a failure; and

executing said selected at least one of said plurality of failure handling routines;

wherein said method controls said receiver station.

29. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including a program;

performing an error correction routine by processing at least a portion of said information transmission;

discerning a failure evidencing an incompleteness of a function; and

executing a failure handling routine in consequence of said step of discerning a failure;

wherein said method controls said receiver station.

30. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of:

receiving an information transmission including processor instructions and a program;

performing an error correction routine by processing at least a portion of said information transmission;

programming said receiver station to perform a failure handling routine in accordance with said processor instructions;

discerning a failure evidencing at least one of incomplete programming and an incorrect program element in said memory by processing information received in said information transmission as corrected in said step of performing; and

executing said failure handling routine in consequence of said step of discerning a failure;

wherein said failure handling routine is performed in accordance with said processor instructions and wherein said method controls said receiver station.

E8 31. (Amended) The method of controlling a receiver station of claim 30, wherein said program comprises mass medium programming.

32. (Unchanged) The method of controlling a receiver station of claim 30, wherein said program comprises computer programming.

33. (Amended) The method of controlling a receiver station of claim 30, further comprising the steps of:

receiving a portion of a failure handling routine from a remote station;

directing said received portion of said failure handling routine to a programmable device; and

storing said portion of said failure handling routine at said programmable device.

34. (Twice Amended) A method of controlling a receiver station, said receiver station including a receiver, a memory operatively connected to said receiver, and at least one processor operatively connected to said memory, said method comprising the steps of: